Designing the optimal bit: balancing energetic cost, speed and reliability

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We consider the challenge of operating a reliable bit that can be rapidly erased. We find that both erasing and reliability times are non-monotonic in the underlying friction, leading to a trade-off between erasing speed and bit reliability. Fast erasure is possible at the expense of low reliability at moderate friction, and high reliability comes at the expense of slow erasure in the underdamped and overdamped limits. Within a given class of bit parameters and control strategies, we define ‘optimal’ designs of bits that meet the desired reliability and erasing time requirements with the lowest operational work cost. We find that optimal designs always saturate the bound on the erasing time requirement, but can exceed the required reliability time if critically damped. The non-trivial geometry of the reliability and erasing time scales allows us to exclude large regions of parameter space as suboptimal. We find that optimal designs are either critically damped or close to critical damping under the erasing procedure.
1. Introduction

Certain information-processing operations such as erasing a bit or copying the state of one bit into another previously randomized bit have fundamental lower bounds on work input \([1–5]\). These lower bounds such as the famous \(k_B T \ln 2\) minimal cost for erasing arise due to equilibrium thermodynamics: there is a need to compensate for any entropy reduction in the information-carrying system with an entropy increase elsewhere. Practical devices, however, do not approach these bounds \([6,7]\) and insights gained from thinking about the lower bound have not yet translated into more energy-efficient technology. A partial explanation is that man-made devices and biological cells need to operate on fast time scales and hence cannot involve the quasi-static manipulations necessary to reach lower bounds \([8,9]\). An alternative suggestion from von Neumann is that the need to store information for long periods of time (reliability) leads to high-cost architectures \([10]\). We explore the interplay between reliability, speed and the energetic cost of bit operation. Equilibrium thermodynamic bounds such as the Landauer limit cannot account for these inherently kinetic phenomena.

This general question of how to design fast, cheap and reliable bits has obvious technological relevance to the optimal design of low-power computational devices \([11–13]\). Additionally, since the discovery of the structure of DNA and the central dogma of molecular biology, it has become well accepted that information processing is at the heart of many natural phenomena. Many authors have explored information processing in biological systems, to both understand natural examples and design synthetic analogues \([3,9,14–19]\). The question of the interplay between reliability, speed and cost are also relevant here, although under-explored.

In this paper, we explore the challenge of building fast, cheap and reliable bits, and provide a framework for its analysis in terms of reliability and erasure time scales. We also take the first steps towards exploring the physics of the optimal design problem by considering a simple model: a particle in a one-dimensional potential, which is a quartic double-well potential in the device’s ‘resting’ state. We require that the bit be reliable, so that a particle equilibrated in either well stays in that well for a specified long time on average. Simultaneously, we require the implementation of an ‘erase’ or ‘reset’ operation using an external control, so that erasure is completed within a specified short amount of time. Our principal question is to find values for the design parameters which consist of the height of the double well, the friction coefficient, and the control parameters to guarantee these requirements without expending more energy than required. Our main contribution is an exploration of this design space, which demonstrates the previously under-appreciated role of friction. In particular, we identify a ‘Goldilocks zone’ where the friction coefficient takes moderate values. This is somewhat counter-intuitive because historically friction has been viewed as a nuisance to computing, to be sent as low as possible \([20–23]\).

In §2, we describe the model which will provide intuition for our work. We formalize the time scale over which the bit stores information through the notion of reliability time. In §2b(i), we describe one simple family of control protocols for resetting a bit. We calculate the work done in erasing a bit for this form of control. We will use this particular control protocol to illustrate our subsequent ideas. In §2b(ii), we introduce the notion of erasing time. In §3, we consolidate from the literature the analytical forms and approximations for our two time scales of interest, and confirm them with numerical simulations. We find that both the reliability and erasing time scales are non-monotonic, roughly U-shaped functions of the friction coefficient. It follows that high reliability is obtained by setting the friction to a low or high value, whereas a low erasing time is favoured by an intermediate value of friction, implying a conflict between the two time scales for a given class of protocols. In §4, we investigate how this conflict feeds into the geometry of optimal bits: bits that fulfil the desired reliability and erasing time requirements with the minimum energy cost. We find and partially characterize a ‘Goldilocks zone’ in design space where optimal bits reside. In §5, we discuss the robustness of our results when more freedom is allowed in the choice of design parameters and the control protocol.
2. The double-well bit

We will represent a device that can store one bit of information by a particle in a symmetric bistable potential $U_{A,B}(x) = A(x^2/B^2 - 1)^2$, where $A$ is the height of the well and $\pm B$ are the coordinates of the minima of the right and left wells. We will refer to the device as a whole as ‘a bit’. The device reports ‘0’ when the particle is in the left well, i.e. $x < 0$, and reports ‘1’ otherwise (figure 1a).

The dynamics of the particle is described by the Langevin equation

$$m \frac{dx}{dt} = p \gamma \frac{dt}{d\tau} + \partial_x U_{A,B}(x) \frac{dt}{d\tau} + \sqrt{2m k_B T} \frac{dW}{d\tau},$$

where $m$ is the mass of the particle, $x$ is the position, $p$ is the momentum, $\gamma$ is the friction coefficient of the medium, $U_{A,B}(x)$ is the potential, $k_B$ is Boltzmann’s constant and $T$ is the temperature of the heat bath. The term $\sqrt{2m k_B T} \frac{dW}{d\tau}$ represents the effect of noise from the surroundings.

The Langevin equation is a stochastic differential equation, to be mathematically interpreted as a Stratonovich integral. For our case, both the Ito and Stratonovich interpretations coincide [24, p. 109] as the noise coefficient $\sqrt{2m k_B T}$ does not depend upon $p$.

In [25, p. 182], the generator for the Langevin equation (2.1) is

$$\mathcal{L} = \frac{p}{m} \partial_x - (\partial_x U_{A,B}(x)) \partial_p + \gamma (-p \partial_p + k_B T \partial_p^2).$$

The Hamiltonian of the system is $H(x, p) = U_{A,B}(x) + p^2/2m$. The Gibbs distribution

$$\pi(x, p) = \frac{e^{-H(x, p)/k_B T}}{\int_{-\infty}^{\infty} \int_{-\infty}^{\infty} e^{-H(x', p')/k_B T} dx' dp'}$$

is approached as the system relaxes to equilibrium. Convergence to $\pi(x, p)$ happens exponentially fast at a rate given by the first non-zero eigenvalue of the generator $\mathcal{L}$ [26].

(a) Reliability

A device to store information should be able to store it with high fidelity for a specified long period of time. We introduce the reliability time to represent the time scale over which our device can store data. Specifically, we define the reliability time $\tau_r$ as the expected first passage time for the particle to cross the barrier of the resting-state potential of the bit, given the Gibbs distribution $\pi(x, p)$ (equation (2.3)) as the initial distribution. That is,

$$\tau_r := \mathbb{E}[\inf\{t \geq 0 \mid x(t) = 0\}],$$

where the expectation is over trajectories $(x(t), p(t))$ distributed as specified by equation (2.1) from the initial condition $(x(0), p(0)) \sim \text{law} \ \pi(x, p)$. Note that $\tau_r$ is also the first passage time to cross the barrier for a bit prepared with a Gibbs distribution, but confined to either the left-hand well $\pi_0(x, p)$ or the right-hand well $\pi_1(x, p)$.

$$\pi_0(x, p) = \begin{cases} 2\pi(x, p) & \text{if } x < 0, \\ 0 & \text{otherwise} \end{cases} \quad \text{and} \quad \pi_1(x, p) = \begin{cases} 2\pi(x, p) & \text{if } x > 0, \\ 0 & \text{otherwise}. \end{cases}$$

Intuitively, once a typical particle has had enough time to reach the top of the barrier, the data stored are no longer reliable.

(b) Setting information

A device intended to store information must provide functionality to load, or set, this information into the device. Setting information is a two-bit operation. A common use case is when a reference bit and the bit to be set are initially at some arbitrary values. We require that after the SET operation the reference bit is unchanged, whereas the bit to be set now holds a copy
Figure 1. A bit as represented by a particle in a one-dimensional potential. (a) The bit in its resting state, with a barrier of height 'A' separating particle locations that correspond to bit values of 0 or 1. (b) A control potential as in example 2.1 is applied to erase the stored data.

(i) Erasing

The example that follows describes a simple family of control potentials to implement the erasing operation for our device, which will form the basis of our analysis. One control potential from this family is illustrated in figure 1b. We chose such a simple class of controls to make a full understanding feasible, setting a framework for analysing more complex protocols. We also note that arbitrary variation of a physical potential in reality is highly non-trivial; experimental studies in which complex time-dependent potentials have been applied in fact use highly dissipated mechanisms to generate ‘effective’ potentials [28,29].

Example 2.1. Our control potentials are described by a single parameter $F \in \mathbb{R}_{>0}$ as follows:

$$V_F(x) := \begin{cases} 
A + F \cdot x - U_{A,B}(x) & \text{if } x \geq 0 \text{ and } A - U_{A,B}(x) + F \cdot x \geq 0, \\
0 & \text{otherwise.}
\end{cases}$$

(2.6)

The Langevin equation in the presence of a control is

$$\begin{align*}
    m \frac{dx}{dt} &= p, \\
    \frac{dp}{dt} &= -\gamma p - \partial_x U_{A,B}(x) dt - \partial_x V_F(x) dt + \sqrt{2m\gamma k_B T} dW.
\end{align*}$$

(2.7)

Note that the control potential, as defined, is not differentiable at the boundary of the region in which it is non-zero. In practice, we assume that $\partial_x V_F$ changes rapidly but continuously in a small vicinity around these points.

of the reference bit. This is the operation that Szilard [1] refers to as ‘copying’ (by contrast, Landauer [2,27] chooses to reserve the word ‘copying’ for the operation where the bit to be set is initially already known to be in the state ‘0’).

Note that in the operation of setting information, or copying in the sense of Szilard, initially the two bits are uncorrelated and unknown, whereas after the operation they are still unknown but correlated. Thus implementing this operation requires decreasing the entropy of the system. Since it is easier to study a one-bit system rather than a two-bit system, we will investigate a one-bit proxy for the task of decreasing the entropy of the system, which is the task of erasing a bit.

Erasing involves taking a device whose initial state is maximally unknown into a known reference state, usually ‘0’. Somewhat counter-intuitively, given the name, erasing increases the information we know about the system. What is erased is not information but randomness. It helps to keep in mind the example of erasing a blackboard where some random state with chalk marks is reset to the ‘all clear’ state.
In this work, we will consider variation of $A$, $F$ and $\gamma$ at fixed $m$, $B$ and $T$, respectively. In this case, $m$ specifies the natural mass scale, $B$ the natural length scale and $k_B T$ the natural energy scale; the natural time scale is then $\sqrt{mB^2/k_B T}$. Henceforth, all numerical quantities will be reported using reduced units defined with respect to these natural scales, although $m$, $B$ and $k_B T$ will be retained within formulae.

(ii) Operational view of erasing

The speed of bit operations is of practical importance: a useful bit must be reliable on much larger time scales than those required to set or switch it. The control is switched on at time $0$ and switched off at an appropriately chosen time $\tau$. The time $\tau$ is chosen beforehand, and does not depend on details of individual trajectories—a trajectory-dependent control would require measurement and feedback that itself would need accounting for [30–35]. We could declare erasing as completed and switch off the control as soon as a majority of the trajectories are retained within formulae.

Despite the robustness of this result to the value of the friction, the heatmaps in figure 2 are friction dependent. When $\gamma$ is low, the particle diffuses very slowly in energy space, and it is the
Figure 2. For particles initiated with \( H(x, p) \leq A - 3k_B T \), well escape times are close to \( \tau_e \). Heat maps show the fractional deviation in expected escape time \( \delta(x, p) \) from the well-thermalized average \( \tau_e \) as a function of initial position \( x \) and momentum \( p \). The labelled contours correspond to a well height \( A = 7 \) with energy \( H(x, p) = A - 3k_B T = 4k_B T \). These heat maps are representative of the situation for other barrier heights \( A \geq 5k_B T \). (a) \( \delta(x, p) \) when \( \gamma = 0.1 \), (b) \( \delta(x, p) \) when \( \gamma = 1 \), (c) \( \delta(x, p) \) when \( \gamma = 10 \) and (d) \( \delta(x, p) \) when \( \gamma = 100 \).

challenge of diffusing within this energy space that prohibits escape from the well. As a result the heatmap corresponding to \( \gamma = 0.1 \) (figure 2a) follows the shape of constant energy contours. As friction starts increasing (e.g. in figure 2b,c), diffusion in momentum space becomes more rapid, but diffusion in position space slows down. Once \( \gamma \) becomes very high (e.g. \( \gamma = 100 \) in figure 2d), the behaviour of the heatmap is essentially determined by the initial position of the particle; those close to the barrier and with \( UA_B(x) \) sufficiently close to \( A \) can escape easily, but the momentum is irrelevant. Using the total energy \( H(x, p) \) as a criterion ensures that we account for all the regimes of friction.

Since we are interested in the typical time scale of transferring particles to a different well from the existent well, we will sample initial points only from the right well. We define the erasing time \( \tau_e \) as the expected time to hit the erasure region, given that the particle started in the right-hand well:

\[
\tau_e = \mathbb{E}[\inf\{t > 0 \mid x(t) < 0 \text{ and } H(x(t), p(t)) \leq A - 3k_B T\}],
\]

where \((x(t), p(t))\) is the solution to equation (2.7) with the initial condition \( (x(0), p(0)) \sim \text{law} \pi_1(x, p) \). Given this definition, \( \tau_e \) indicates a typical time scale over which the control must be applied to successfully erase a large fraction of the bits. In practice, the control would be applied for a period \( \tau > \tau_e \) to achieve high accuracy. We will use \( \tau_e \) as an indicative time scale of the control operation for the purposes of our analysis. It is useful to decompose the erasing time \( \tau_e \) as the sum of two times: the transport time and the mixing time.

- Transport time \( \tau_t \). The time taken by the particle to reach well ‘0’ given that it is initially distributed according to \( \pi_1(x, p) \),

\[
\tau_t = \mathbb{E}[\inf\{t > 0 \mid x(t) \leq 0\}],
\]

where \( x(t) \) is the solution to equation (2.7) with the initial condition \( (x(0), p(0)) \sim \text{law} \pi_1(x, p) \).
— Mixing time ($\tau_m$). The time taken by the particle to mix sufficiently inside the well. This is the time starting from when the particle first reaches well '0' to when it first hits the erasure region,

$$\tau_m = \tau_e - \tau_t.$$  

(2.11)

(iii) Cost of erasing

In this section, we calculate the work done in erasing a bit. From Sekimoto’s expression [38,39], for a protocol applied for a time $\tau$ and with a region of effect $I = \{x \geq 0 \mid A - U_{A,B}(x) + F \cdot x \geq 0\}$,

$$\langle W \rangle = \int_0^\tau \int_{x \in I} \frac{\partial V_F(x,t)}{\partial t} p(x,t) \, dx \, dt,$$  

(2.12)

where $p(x,t) \, dx \, dt$ is the probability that the particle is between position $(x, x + dx)$ in the time interval $(t, t + dt)$. There are two potential sources of work that appear in our calculation.

1. When we begin the erasure protocol by switching on the control to lift the particle.
2. At the end of the protocol when we switch off the control.

We note that, in our family of controls, there is negligible energy recovered when the control is switched off (see electronic supplementary material, §3.2), since the probability of the particle being in the region in which the control is applied is small. More generally, the question of whether energy might be recovered from small systems and stored efficiently is a complex one, despite the optimism shown in previous discussions of erasing. Indeed, current technology does not attempt to recover any energy from bits.

We now calculate the work done for our protocol (example 2.1). The particle’s initial potential energy is approximately $k_B T/2$ on average, due to the equipartition theorem, and after the control is switched the average potential energy is $A + F \cdot B$ for a particle in the right well, since the particle is localized around $x = B$, and still $k_B T/2$ for a particle in the left well. So, ignoring energy recovery at the end of the operation, the net work done for the erasure protocol is $W = (A + F \cdot B - k_B T/2)/2$. As justified analytically and numerically in the electronic supplementary material, §3.1, this approximation is accurate for the values of $A$ and $F$ that we consider, and we will use this as the form of work for the rest of the manuscript.

**Observation 2.2.** Work is an increasing function of well height $A$ at fixed $F$ and $\gamma$. This follows immediately from the expression of work $W = (A + F \cdot B - k_B T/2)/2$.

3. Friction-based trade-offs for reliability and erasing

We explore the behaviour of the reliability and erasing time scales as functions of the friction coefficient. We find that both these time scales are non-monotonic, roughly U-shaped functions of the friction coefficient. A high reliability time requirement is favoured by a very low or very high friction, whereas a low erasing time requirement is helped by the choice of a moderate value of friction. Since a bit designer would seek reliable bits (needing high or low friction) that can be erased fast (needing intermediate friction), this yields a friction-based trade-off between reliability and speed of erasure.

(a) Reliability time

Our definition of reliability time (equation (2.4)) is very similar to the classic problem of escape rates from one-dimensional wells (figure 3), as applied in transition state theory to understand chemical reactions. In [40], Kramers found analytic expressions for the escape rate $k$ from a well by calculating the flux of particles between a source on one side of the barrier ($x_A$) and a sink on the other side ($x_B$). Kramers’ expressions apply separately to the regimes of low friction, moderate to high friction and very high friction. Later the groups of Mel’nikov & Meshkov [41] and Pollak
Figure 3. The escape of particles from a one-dimensional well. Kramers [40] considered a source of particles at the bottom of the well, and estimated the rate of escape to a sink on the far side of a barrier.

Figure 4. The reliability time $\tau_r \propto 1/\gamma$ in the low-friction regime, $\tau_r \propto \gamma$ in the high-friction regime and is minimum at moderate friction. Simulation results are compared with the inverse of the escape rate from a single well $(1/k)$ and $(1/2k)$, as predicted by equation (3.1). Here, and elsewhere in the manuscript, error bars are omitted when comparable to data points. (a) $A = 6$ and (b) $A = 10$.

e et al. [42] gave formulae that interpolate accurately over all values of friction (see the review in [43]). We will apply the result of Mel’nikov and Meshkov to estimate analytical forms of the escape rate for our bistable system

$$k = \frac{\omega_0^2}{2\pi} \left[ \sqrt{1 + \frac{\gamma^2}{4\omega_b^2}} - \frac{\gamma}{2\omega_b} \right] g e^{-A/k_BT}, \quad \text{where}$$

$$\ln g = \frac{1}{2\pi} \int_{0}^{\pi} \ln \left[ 1 - \exp \left( -\frac{\gamma I(A)}{4k_BT \cos^2 x} \right) \right] \, dx.$$  (3.1)

Here, $\omega_b$ is the angular frequency at barrier height, $\omega_0$ is the angular frequency at the bottom of the well and $I(A)$ is the action for barrier height $A$ (see the electronic supplementary material, §5, for a detailed definition of these parameters and calculations for our system).

We plot the analytical prediction of $1/k$ given by equation (3.1) in figure 4 for two values of well height $A$, as a function of friction $\gamma$. This prediction is compared with average first passage time for particles to reach the top of the barrier from an initial Boltzmann distribution within a single well. The two quantities differ at large $\gamma$ because Kramers’ definition does not treat a particle that crosses the barrier but then immediately crosses back as having ‘escaped’, whereas our definition of reliability in terms of a first passage time treats such particles as no longer being reliable. In
the underdamped regime, immediate recrossings are rare and hence $\tau_r$ and $1/k$ coincide; in the overdamped regime, particles that reach the barrier top have a 50% chance of returning and so $\tau_r = 1/2k$. As can be seen from figure 4, $\tau_r$ smoothly interpolates between $1/k$ and $1/2k$, with the small numerical factor providing only a minor correction to the underlying physics of the analytical expression in equation (3.1).

The Mel'nikov–Meshkov expression predicts an almost-exponential scaling of $1/k$ with barrier height $A$, which is reproduced by $\tau_r$ and expected from the Arrhenius rate law [44]. Note that both $1/k$ and $\tau_r$ are non-monotonic in friction $\gamma$, with long reliability times in the underdamped and overdamped limits. This behaviour results from the need for particles to diffuse in both position and energy in order to reach the top of the barrier from an initial state thermalized within a single well. At high friction, particles rapidly sample different kinetic energies due to strong coupling with the environment, but move slowly in position space and hence take a long time to cross the barrier. At low friction, particles can move rapidly but their energy remains effectively constant over short time periods. They only cross the barrier when they have eventually gained enough total energy. Intermediate friction, when neither process is excessively slow, gives the shortest $\tau_r$. This behaviour is typical of equilibrating systems in which an initial out-of-equilibrium condition (particles are guaranteed to be on one side of the well and not on the other) relaxes towards an equilibrium state (particles on both sides of the barrier), and is thus insensitive to the details of our bit design.

A more detailed analysis of the dependence of the reliability time on various parameters, and indeed the functional form of the well, is possible. However, these details are not necessary for the conclusions we draw in the rest of this manuscript, and hence we omit them here.

(b) Erasing time

As noted earlier, the erasing time is composed of two parts: the transport time defined in equation (2.10) and the mixing time defined in equation (2.11). We now present analytical estimates of these times and compare them with numerical solutions.

(i) Transport time

We can obtain an analytical estimate of the transport time in low- and high-friction limits by assuming that a particle starting at $x = B$ moves deterministically under the influence of the potential slope and drag force.

1. **Low-friction regime.** The particle travels with a constant acceleration of $\frac{F}{m}$ and the time taken to travel a distance $B$ is $\tau_t \approx \sqrt{\frac{2mB}{F}}$.

2. **High-friction regime.** In this regime, we assume that the net force on the particle (arising from the sum of drag and potential) is zero. The particle travels with a velocity of $\frac{F}{m\gamma}$, and the time taken to travel a distance $B$ is $\tau_t \approx \frac{mB\gamma}{F}$.

We thus expect the transport time to be constant in the underdamped regime and increase linearly with friction in the overdamped regime. Figure 5 illustrates that this scaling is observed in Langevin simulations, and that numerical values are in reasonable agreement with these crude estimates. The largest quantitative deviations occur at low force and low friction (e.g. $F = 1$ in figure 5a), when the diffusion of the particle on the slope contributes significantly to $\tau_t$. This results in a simulation transport time larger than the analytical estimate.

(ii) Mixing time

Similar to the transport time, analytical estimates of the mixing time can be obtained in the limits of high and low friction.
in the high-friction limit. In the first case, mixing within the well is limited by the rate at which the particle can reduce its total energy, whereas, in the second, it is determined by the barrier at 

\[ \gamma \ln(\gamma) \]

Equation (3.2) and (3.6) predict that the mixing time will scale as \( 1/\gamma \) in the low-friction limit and as \( \gamma \) in the high-friction limit. In the first case, mixing within the well is limited by the rate at which the particle can reduce its total energy, whereas, in the second, it is determined by the
speed with which the particle can diffuse in position space to a configuration with lower potential energy. We plot simulation results for the mixing time, along with the analytic predictions, in figure 6, confirming this scaling and the resultant non-monotonicity. Quantitatively, simulation results deviate from the crude analytic predictions at low force (e.g. $F = 1$ in figure 6(a)), when it is no longer reasonable to treat $x = 0$ as either a reflecting barrier or a steep side of a harmonic well. Instead, excursions of the particle back onto the slope occupying the region $x > 0$ lead to much larger mixing times. Nonetheless, the scaling and non-monotonicity in friction are preserved. Combining $\tau_{\text{trans}}$ and $\tau_{\text{mix}}$ gives $\tau_e$, plotted in figure 7. Analytically, the erasing time is given as:

1. Low-friction regime:

$$\tau_e \approx \sqrt{\frac{2mB}{F}} + \frac{1}{\gamma} \log \frac{A + F \cdot B}{A - 3k_B T}. \quad (3.7)$$

2. High-friction regime:

$$\tau_e \approx \frac{mB \gamma}{F} + \frac{mB^2 \gamma}{2\sqrt{2A}}. \quad (3.8)$$

Like reliability, erasing time is large in the underdamped and overdamped limits, and minimized at intermediate values of friction. The physical cause is the same as before; our erasing protocol involves setting the system into a non-equilibrium state, and waiting for the system
Figure 8. Comparison of transport and mixing times. Transport time dominates the mixing time for low force at high friction. 
(a) $A = 10, F = 1$ and (b) $A = 10, F = 100$.

A larger value of $A$ implies a steeper descent into the target left-hand well, making mixing faster. We therefore expect that the mixing time, and hence the erasing time, monotonically decreases with $A$.

Observation 3.1. The erasing time is a strictly decreasing function of well height $A$ at fixed $F$, $\gamma$. This can be seen from the analytic expressions of erasing time (equations (3.7) and (3.8)) backed up with numerical simulations (figure 9).

By contrast, erasing time shows a non-monotonic dependence on $F$ at fixed $A$, $\gamma$. Applying too little force leads to slow transport, and does not effectively trap the particle within the target well. But applying too much force supplies the particle with too much energy, which must subsequently be lost during the mixing period. The fact that erasing time monotonically decreases with $A$ at fixed $F$ and $\gamma$, and shows a non-monotonic dependence on $F$ at fixed $A$ and $\gamma$, leads to a non-monotonic dependence of $\tau_e$ on $F$ at fixed $W = A + F$ and $\gamma$. We illustrate this non-monotonicity in figure 10, in which simple regression formulae have been fitted to the simulation data to enable interpolation at fixed $W$ and $\gamma$ (see electronic supplementary material, §4). As friction increases, the force required to provide the particle with excess energy increases, leading to minima at higher values of $F$. 

...to relax towards an equilibrium in the perturbed potential. This process requires the system to diffuse in energy space and also explore configuration space, and is therefore favoured by intermediate friction. Specifically, if the friction is too low, the particle oscillates and slowly loses energy to be confined within the desired well. If the friction is too high, both the transport and mixing times increase as the particle’s movement through space is so slow. The relative importance of these effects can be seen in figure 8. We note that the value of the damping $\gamma$ that minimizes $\tau_e$ is quite sensitive to $F$ (figure 8). Fundamentally, a larger $F$ means the challenge of moving in position space is made easier, and a greater loss of energy is needed to reach equilibrium. Therefore, a higher friction coefficient is optimal. As with the reliability time, further analysis is possible but not necessary for the conclusions we wish to draw. Once again, the key point is the trade-off between high and low friction, which is not specific to our control. Indeed, it is likely to be quite generic since any protocol will necessarily push the system out of equilibrium, and will require particles to be typically confined within the target well before the control is removed.

Both erasing and reliability times exhibit a trade-off in friction, being minimized by intermediate values. This fact sets up a second trade-off between designing bits with extreme values of friction to optimize reliability, or moderate values of friction to optimize erasing. The consequences of this secondary trade-off will be explored in §4.

(iii) Additional dependencies of the erasing time

A larger value of $A$ implies a steeper descent into the target left-hand well, making mixing faster. We therefore expect that the mixing time, and hence the erasing time, monotonically decreases with $A$.

Observation 3.1. The erasing time is a strictly decreasing function of well height $A$ at fixed $F$, $\gamma$. This can be seen from the analytic expressions of erasing time (equations (3.7) and (3.8)) backed up with numerical simulations (figure 9).
Figure 9. Evidence that the erasing time is a strictly decreasing function of well height across a range of $F$ and $\gamma$. Other values of $F$ and $\gamma$ show similar behaviour. $(a) F = 5$ and $(b) F = 100$.

Figure 10. For a fixed value of $W$ and $\gamma$, the erasing time is a non-monotonic function of $F$ and is minimum at moderate $F$. This is illustrated at work $W = 20$ for various values of $\gamma$. $(a) \gamma = 0.1$, $(b) \gamma = 1$, $(c) \gamma = 10$ and $(d) \gamma = 100$.

We make the following observation, which will be used in the subsequent section.

**Observation 3.2.** We have found no evidence of multiple local minima of erasing time in a level set of work for our control family (see the electronic supplementary material, §6, for characteristic plots showing the minima of erasing time in a level set of work). Physically, this is unsurprising as the non-monotonicity in $\tau_e$ with $\gamma$ and $F$ mentioned above arises from fairly simple trade-offs, producing curves with single minima.

As with the reliability time, a more detailed analysis of the dependence of $\tau_e$ on other parameters, and even the shape of the control, is possible. However, these details are likely to be difficult to generalize, and are not necessary for the conclusions we draw in the subsequent sections.
4. Design of bits

We are now ready to study the question of how to design good bits. A design involves choosing parameters $A, F, \gamma$ for a bit to satisfy requirement specifications in terms of speed of erasing and reliability, without expending more work than required. The most general formulation of our problem would require us to also allow the length scale $B$, the temperature $T$ and the mass $m$ to vary, as well as allowing arbitrary controls. Such a formulation would appear to make the problem even more challenging, so it seems prudent to restrict our first analysis to the variables $A, F$ and $\gamma$. Our restricted analysis is not without value since the underlying technology in any given construction typically does not allow arbitrary variation. Our numerical analysis with example 2.1 will guide us in our assumptions and analyses, but our results will hold in greater generality. We will construct our proofs based on general assumptions, and subsequently explain how these assumptions are met by our control family.

We introduce the following terms.

1. The design of a bit is completely specified by the design triple $(A, F, \gamma)$. Design space (DS) is the space of all design triples $(A, F, \gamma)$.

2. A requirement specification is a tuple $(t_r, t_e) \in \mathbb{R}^2_0$ denoting the reliability and erasing time that we require of the bit. Requirement space (RS) is the space of all requirement specifications.

3. Erasing time $\tau_e: DS \rightarrow \mathbb{R}_{>0}$ takes a design triple $(A, F, \gamma)$ to the time required for erasing the corresponding bit under the control protocol specified by $F$. Reliability time $\tau_r: DS \rightarrow \mathbb{R}_{>0}$ takes a design triple $(A, F, \gamma)$ to the reliability time of the corresponding bit. Note that $\tau_r$ is constant as a function of $F$ as it is a property of the dynamics in the absence of control.

4. Work $W: DS \rightarrow \mathbb{R}_{>0}$ represents the expected work done by the control in erasing the corresponding bit. We will assume that $W$ is constant as a function of $\gamma$, as is the case in example 2.1.

5. A design $(A, F, \gamma)$ is feasible for a requirement $(t_r, t_e)$ iff both $\tau_r(A, F, \gamma) \geq t_r$ and $\tau_e(A, F, \gamma) \leq t_e$. A $(t_r, t_e)$-feasible design $(A, F, \gamma)$ is $(t_r, t_e)$-optimal iff the work $W(A, F, \gamma)$ is minimum among all $(t_r, t_e)$-feasible designs.

6. Inspired by the observation that non-trivial minima of erasing time at fixed work exist for our family of protocols (§3b(iii)), we define the notion of trapped bits. A design $(A, F, \gamma)$ is trapped iff for all designs $(A', F', \gamma')$ with $W(A, F, \gamma) = W(A', F', \gamma')$ the erasing time $\tau_e(A, F, \gamma) \leq \tau_e(A', F', \gamma')$. A design $(A, F, \gamma)$ is uniquely trapped iff for all designs $(A', F', \gamma')$ with $W(A, F, \gamma) = W(A', F', \gamma')$ the erasing time $\tau_e(A, F, \gamma) \leq \tau_e(A', F', \gamma')$ with equality iff $(A, F, \gamma) = (A', F', \gamma')$. A design $(A, F, \gamma)$ is locally trapped iff there exists a neighbourhood of $(A, F, \gamma)$ consisting of bits $(A', F', \gamma')$ with $W(A, F, \gamma) = W(A', F', \gamma')$ such that the erasing time $\tau_e(A, F, \gamma) \leq \tau_e(A', F', \gamma')$. More informally, a trapped design has the lowest erasing time within a level set of work; a trapped design is unique if it is the only design within that level set of work to have the minimal erasing time; and a locally trapped design has the minimal erasing time within a local neighbourhood of designs of equal work.

7. A requirement specification $(t_r, t_e)$ is unsaturated iff there exists a $(t_r, t_e)$-optimal design $(A, F, \gamma)$ such that either $\tau_r(A, F, \gamma) > t_r$ or $\tau_e(A, F, \gamma) < t_e$. A feasible requirement specification that is not unsaturated is called saturated.

Throughout this section, we will assume that $\tau_e, \tau_r$ and $W$ are continuous functions.

We will state the main results related to the properties of the optimal design leaving the detailed proofs to the electronic supplementary material. We first claim that an optimal design always saturates the bound on the erasing time constraint. Further, if the optimal bit is not locally trapped, then it also saturates the bound on the reliability time constraint.

Claim 4.1 (saturation of time scales). Let us assume that it is possible to locally decrease work at a fixed reliability time. (This is generally possible since one can perturb the control parameters
to reduce work; but reliability time does not depend on the control parameters.) Fix requirement specifications \((t_e, t_r) \in \mathcal{R}S\). Suppose \((A, F, \gamma)\) is a \((t_r, t_e)\)-optimal design. Then

1. \(\tau_e(A, F, \gamma) = t_e\).
2. If the design \((A, F, \gamma)\) is not locally trapped, then \(\tau_r(A, F, \gamma) = t_r\).

**Proof.** See the electronic supplementary material, §7.

The next claim provides insight into the geometry of optimal designs. In particular, it states that under mild assumptions the requirement space is divided into two regions by a boundary given by the reliability and erasing times of trapped designs. Requirements with \(t_r < t'_r\) and \(t_e = t'_e\), where \((t'_r, t'_e)\) is a requirement on the dividing line, are unsaturated, while other requirement specifications are saturated.

**Claim 4.2 (saturated and unsaturated requirements).** Assume that the erasing time of trapped designs is a strictly decreasing function of the work (see the electronic supplementary material, observation 7.1, for a justification), and that as before it is always possible to decrease work at a fixed reliability time. Let \((A^*, F^*, \gamma^*)\) be a trapped design such that \(\tau_e(A^*, F^*, \gamma^*) = t_e\).

1. If \(t_r \leq \tau_r(A^*, F^*, \gamma^*)\) then \((A^*, F^*, \gamma^*)\) is \((t_r, t_e)\)-optimal.
2. If \(t_r < \tau_r(A^*, F^*, \gamma^*)\) then \((t_r, t_e)\) is unsaturated.
3. Make the additional assumption that locally trapped designs are uniquely trapped (as noted for our family of protocols (example 2.1) in observation 3.2). If \(t_r \geq \tau_r(A^*, F^*, \gamma^*)\), then \((t_r, t_e)\) is saturated.

**Proof.** See the electronic supplementary material, §7.

The claims about saturation/unsaturation of time scales can also be proved using Karush–Kuhn–Tucker (KKT) conditions (see the electronic supplementary material, §7), a standard tool from optimization theory.

A more intuitive picture of the results can be understood from figure 11. In this figure, we illustrate how finding an optimal design subject to a specification maps a point in the requirement space to a point in the design space. For a trapped design \((A^*, F^*, \gamma^*)\), requirements with \(t_r < \tau_r(A^*, F^*, \gamma^*)\) and \(t_e = \tau_e(A^*, F^*, \gamma^*)\) are unsaturated and get mapped to the same design \((A^*, F^*, \gamma')\) (claims 4.2(2) and 4.2(1)). If the design \((A^*, F^*, \gamma^*)\) is uniquely trapped, then requirements with \(t_r \geq \tau_r(A^*, F^*, \gamma^*)\) and \(t_e = \tau_e(A^*, F^*, \gamma^*)\) are saturated (claim 4.2(3)).

**Figure 12** illustrates these results for our example family of controls (example 2.1). As discussed in the electronic supplementary material, §4, we have implemented simple regression to fit the functions \(\tau_e(.)\) and \(\tau_r(.)\) to our simulation results. We then identified trapped designs using numerical minimization, plotting the requirement specifications saturated by these designs. For each trapped bit \((A^*, F^*, \gamma^*)\), we randomly selected requirements with \(t_e = \tau_e(A^*, F^*, \gamma^*)\), but with \(t_r\) either greater than, equal to or less than \(\tau_r(A^*, F^*, \gamma^*)\), and used numerical optimization techniques to search for the optimal designs. The results support our analysis; requirements with \(t_r < \tau_r(A^*, F^*, \gamma^*)\) are unsaturated, and those with \(t_r \geq \tau_r(A^*, F^*, \gamma^*)\) are saturated. Furthermore, as we show in **figure 12b**, unsaturated requirements at fixed \(t_e\) all map to the same trapped design.

**(a) Optimal friction for simple controls**

In §3, we demonstrated that both reliability and erasing times are non-monotonic in friction, with short erasing times favoured by moderate values of friction, and long reliability times favoured by extreme values. In what follows, we give a precise quantification of the resultant trade-off in finding the friction of an optimal bit. The analysis is significantly simplified for our family of controls, in which work is independent of the friction coefficient.
(a) requirement space (RS)

(b) design space (DS)

Figure 11. An illustration of the mapping of requirement specifications to optimal designs. The design space (DS) is divided by a curve corresponding to the reliability and erasing times of trapped designs. Points M, N in (a) requirement space (RS) having the same erasing time requirement get mapped to the same optimal bit in (b) design space: a trapped design with \( \tau_r \) and \( \tau_e \) equal to the requirements at N. The requirement specifications represented by points like P having the same \( \tau_e \) but greater \( \tau_r \) than N are mapped to distinct points in design space. (c) A representation of a level set of work \( W \) within the design space, illustrating that the optimal designs to which unsaturated requirements are mapped minimize the erasing time among all designs requiring the same work.

Figure 12. Illustration of the division of requirement space (RS) into saturated and unsaturated regions by requirements that correspond to trapped designs. (a) Squares show requirements \((t_r, t_e)\) that are saturated by trapped designs for the family of protocols we consider. Numerical optimization shows that requirements to the left of the locus defined by these points are unsaturated (circles), whereas requirements to the right are saturated (diamonds). (b) A plot of the optimal designs for points from (a) at \( t_e = 1.5967 \). It is clear that for requirements \( t_e \leq 3173 \), lying to the left of the trapped-design locus in (a), optimal design parameters are identical, whereas they are distinct for \( t_e > 3173 \).

Let us introduce the following terms. Fix \( A \) and \( F \). Then,

1. \( \gamma_{\text{crit}}^e \) is the friction coefficient that minimizes the erasing time as a function of friction coefficient \( \gamma \) at fixed \( A \) and \( F \), i.e. for all \( \gamma' \in \mathbb{R}_{>0} \), we have

\[
\tau_e(A, F, \gamma_{\text{crit}}^e) \leq \tau_e(A, F, \gamma').
\] (4.1)
We call the design \((A, F, \gamma_{\text{crit}}^e)\) critically damped.

2. \(\gamma_{\text{crit}}^r\) is the friction coefficient that minimizes the reliability time as a function of friction coefficient \(\gamma\) at fixed \(A\) and \(F\), i.e. for all \(\gamma' \in \mathbb{R}_{>0}\), we have
\[
\tau_r(A, F, \gamma_{\text{crit}}^r) \leq \tau_r(A, F, \gamma').
\]

(4.2)

It is easy to note that trapped bits are also critically damped. In figure 13, we show illustrative curves of the erasing and reliability times as a function of friction coefficient \(\gamma\) at fixed \(A, F\). These curves have single minima at \(\gamma_{\text{crit}}^e\) and \(\gamma_{\text{crit}}^r\) respectively. Also shown on these graphs are regions of friction space that can be eliminated from consideration for optimal bits. To eliminate extreme values of friction, we note that the design must have a minimal finite \(A\) to be a well-defined two-state system in the resting state. For our bit, it is \(A_{\text{min}} \approx 3\). In the next claim, we precisely describe which regions of friction can be eliminated.

Claim 4.3 (forbidden regions for optimal friction). Assume that both \(\tau_e\) and \(\tau_r\) have a single, well-defined minimum and tend to infinity as \(\gamma\) tends to zero or infinity. Let \((A, F, \gamma)\) be a \((\tau_e, \tau_r)\)-optimal design (see figure 13 for notational convenience).

1. Let \(\gamma_0\) be such that \(\tau_r(A, F, \gamma_0) = \tau_r(A, F, \gamma_{\text{crit}}^e)\).
   - (a) If \(\gamma_{\text{crit}}^e > \gamma_{\text{crit}}^r\), then \(\gamma \notin (\gamma_0, \gamma_{\text{crit}}^e)\).
   - (b) If \(\gamma_{\text{crit}}^e < \gamma_{\text{crit}}^r\), then \(\gamma \notin (\gamma_{\text{crit}}^e, \gamma_{\text{crit}}^r)\).

That is, the friction of the optimal bit does not reside in the central red region in figure 13.

2. Let \(A_{\text{min}}\) be the minimum height for a bit to be meaningfully bistable and let \(\gamma_1 < \gamma_2\) be such that \(\tau_r(A_{\text{min}}, F, \gamma_1) = \tau_r(A_{\text{min}}, F, \gamma_2) = \tau_r\). If \((A, F, \gamma)\) is not locally trapped, then \(\gamma \notin (0, \gamma_1) \cup (\gamma_2, \infty)\).

That is, the friction of the optimal bit does not arise from the extreme red regions in figure 13.

Proof:

1. We prove it for the case when \(\gamma_{\text{crit}}^e > \gamma_{\text{crit}}^r\), the other case proceeds in identical fashion.
   - For contradiction, assume that \(\gamma \in (\gamma_0, \gamma_{\text{crit}}^e)\). Then, due to the single minima in both
erasing time requirements, the designs become saturated and the optimal friction lies adjacent to critical damping, for our family of controls. We plot friction for optimal designs \((A, F, \gamma')\) against erasing time requirements \((t_e)\) for a fixed value of reliability time requirement \((t_i)\), alongside \(\gamma^e_\text{crit} \) and \(\gamma^r_\text{crit}\). Note that \(A\) and \(F\) are not fixed, but determined by the optimization procedure alongside the optimal friction for each requirement \((t_i, t_e)\). The data were obtained from numerical optimization and minimization based on regression fits to simulation data. (a) Reliability time requirement \((t_i) = 500\) and (b) reliability time requirement \((t_i) = 10000\).

\(\tau_e\) and \(\tau_r\), and the fact that \(\tau_r\) tends to infinity as \(\gamma\) tends to zero or infinity, there exists a design \((A, F, \gamma')\) with \(\gamma' > \gamma_0\) and \(\tau_r(A, F, \gamma') = \tau_r(A, F, \gamma) \geq \tau_r\) but \(\tau_e(A, F, \gamma') < \tau_e(A, F, \gamma) \leq \tau_e\). The design \((A, F, \gamma')\) is \((\tau_r, \tau_e)\)-optimal since it is \((\tau_r, \tau_e)\)-feasible and has \(W(A, F, \gamma') = W(A, F, \gamma)\), contradicting lemma 4.1(1) that the optimal bit saturates the bound on the erasing time constraint.

2. For contradiction, suppose that \(\gamma < \gamma_1\) or \(\gamma > \gamma_2\). Then since \(A \geq A_{\min}\) and the reliability time increases with well height and more extreme values of \(\gamma\), either \(\tau_r(A, F, \gamma) = \tau_r(A_{\min}, F, \gamma_1) = \tau_r\) or \(\tau_r(A, F, \gamma) \geq \tau_r(A_{\min}, F, \gamma_2) = \tau_r\), contradicting claim 4.1(2) that an optimal design that is not locally trapped saturates the bound on the reliability time constraint.

For clarity, let us assume initially that \(\gamma^e_\text{crit} > \gamma^r_\text{crit}\) (equivalent arguments hold for the alternative). We see that optimal designs either reside at \(\gamma^e_\text{crit}\) or lie within two regions at moderate friction, as illustrated in figure 13. Interestingly, one region is adjacent to \(\gamma^e_\text{crit}\), whereas the other is not. It is not easy to see how designs in one region \((\gamma_1 \leq \gamma \leq \gamma_0)\) as in figure 13 can outperform those in the other region \((\gamma^e_\text{crit} < \gamma \leq \gamma_2)\). Indeed, when we performed numerical optimization on the regression-based fits to our simulation data, we only observed optimal bits that are either critically damped or lie in the allowed region adjacent to critical damping. This is illustrated in figure 14, where we plot the optimal friction as a function of erasing time requirement at fixed reliability time requirement, for two values of reliability time requirements. We also plot \(\gamma^e_\text{crit}\) and \(\gamma^r_\text{crit}\) for comparison. At low erasing time requirements, designs reside at \(\gamma^e_\text{crit}\). At slightly higher erasing time requirements, the designs become saturated and the optimal friction lies adjacent to \(\gamma^e_\text{crit}\) in the region \(\gamma^e_\text{crit} < \gamma \leq \gamma_2\). Eventually, \(\gamma^e_\text{crit}\) crosses \(\gamma^r_\text{crit}\). At the crossing point, we have \(\gamma = \gamma^e_\text{crit} = \gamma^r_\text{crit}\). At higher values of erasing time requirements, \(\gamma\) still occupies the region adjacent to \(\gamma^e_\text{crit}\) which is now \(\gamma_1 \leq \gamma \leq \gamma^e_\text{crit} < \gamma^r_\text{crit}\).

5. Conclusion

We have explored the question of the design of optimal bits. Previously, authors have focused on designing optimal protocols that minimize work input when implementing a finite-time operation on a given system [8,45–48]. Our approach differs in considering that bits need to have two distinct functionalities: retain data for long periods of time and allow rapid switching or erasing. Moreover, we consider optimizing over system parameters such as the intrinsic friction...
Our fundamental observation is that friction plays a non-trivial role in the design of bits. Both switching/erasing and the eventual degradation of data involve relaxation towards equilibrium from a non-equilibrium distribution. This process is fastest at intermediate values of the friction, but slow in the overdamped and underdamped regimes. The best bit designs have high reliability times and low switching/erasing times, which implies an inherent trade-off in bit design between extreme values of friction that favour high reliability, and moderate values of friction that favour rapid switching or erasing.

We have explored the consequences of the biphasic role of friction for a simple class of controls. The existence of non-trivial minima of erasing time in the level set of work leads to the generation of trapped designs. These designs are optimal for reliability requirements smaller than their own reliability time leading to unsaturated requirements. The result of the trade-off between extreme values of friction that maximize reliability time and moderate values of friction that minimize erasing times is that optimal designs are either critically damped or occupy a region of moderate friction close to critical damping.

Our work opens up a new perspective on the design of efficient computational devices showing that: the best designs are likely to be neither underdamped nor overdamped. This observation is particularly important as some authors have considered friction to be inherently problematic for computation [20–23]. Equally, the role of friction is suppressed when bits are modelled as discrete two-state systems [2,9,49], since this approximation assumes rapid equilibration within the discrete states.

We have only considered a simple family of controls to motivate our analysis and illustrate our findings. This family is not optimal—it was chosen for its simplicity and ease of analysis. Moreover, there is some arbitrariness in the definition of both the erasing and reliability times. As such, the numerical details of the results obtained are not very important. We are not claiming to have derived numerical corrections to the minimal cost of erasing a bit, for example, or the specific work costs (substantially larger than $k_B T \ln 2$) which are not that informative. Rather, it is the qualitative results, which hold for a much broader class of controls, that are important. The non-monotonic role of friction in both the erasing and reliability time scales is a generic physical phenomenon that extends beyond the details of our implementation, and implies a competition between the goals of fast manipulation and long reliability times. Relatively weak assumptions—that it is always possible to decrease work at fixed reliability time and that the minimal erasing time decreases with increased work—imply that erasing time requirements are always saturated by optimal bits and that trapped designs lead to unsaturated reliability time requirements, respectively. Other results rely more on the simplicity of the control family: the existence of only one local minimum of erasing time at fixed work simplifies the question of whether a requirement specification is saturated. The fact that work is independent of friction simplifies the task of eliminating certain values of friction as suboptimal.

Explicit exploration of a broader class of controls, including those with more complex variation over time, and varying parameters such as particle mass and distance between wells, are possible directions for future work. It is not immediately clear whether minima in erasing time at fixed work cost will become more or less prominent features of the optimization landscape when the complexity of the system is increased, for example. In particular, raising or lowering the barrier between metastable states is a common idea [8,9,28,29]. Lowering the barrier during erasing potentially allows for faster erasing at fixed reliability time and lower work cost. If said barriers could be raised and lowered arbitrarily far and quickly, it may be possible to circumvent any conflict between high reliability and low erasure time. However, real physical systems are not generally this flexible. Indeed, in order to apply a complex time-dependent control to a small colloid, experimenters typically use optical feedback traps [28,29], which are not true potentials and rely on the continuous input of energy to apply forces and perform feedback control. For true physical protocols that permit finite raising and lowering of barriers between metastable states, we expect that our findings would still apply to a family of protocols with optimal barrier manipulation. An alternative direction would be to consider similar effects in systems with inherently quantum mechanical behaviour.
Data accessibility. The codes and computational data for this manuscript can be downloaded from https://www.imperial.ac.uk/principles-of-biomolecular-systems/contact–obtain-code-and-data/.

Authors’ contributions. M.G. conceived the project. A.D. performed the calculations. A.D., M.G., T.E.O. and N.S.J. planned the research, analysed the results and wrote the paper.

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